

ABSTRACT

Transmitting data on a serial data transmission path includes reading at least one bit of a device address, wherein the device address is coupled with a serial data word and the serial data word is transmitted across a first link of a first serial data transmission path to a first device having an address. The serial data word is passed across a second link of the first serial data transmission path to a second device if the at least one bit of the device address does not match at least one corresponding address bit of the first device.

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